

IN THE UNITED STATES PATENT OFFICE

Applicants: Jonathan B. Ballagh et al.
Assignee: Xilinx, Inc.
Title: Clock Stabilization Detection For Hardware Simulation
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Examiner: Andre Pierre Louis Art Unit: 2123
Docket No.: X-1192 US Conf. No.: 7556

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION AFTER RCE

In response to the Office Action mailed from the Patent Office on September 17, 2007, please replace/substitute the following claim(s) as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.